

Objectives

- 1. Make an LED blink on your PSoC 4 Kit using only the ARM Cortex-M0 CPU
- 2. Use PSoC Creator to create, modify and program PSoC 4 projects

Requirements	Details
Hardware	CY8CKIT-042 PSoC 4 Pioneer Kit
Software	PSoC Creator 3.0 SP 1.0 or Higher
Firmware	None (Create your own project)
Components used	Pin Component

Block Diagram





Theory

The goal of this lab is to learn the basics of the PSoC Creator IDE by implementing a simple, firmware controlled blinking LED. A PSoC Creator project is created, A pin component is placed and configured, the pin is assigned and firmware is written. The project is then programmed onto the development board and observed.

The LED is connected to P1[6], and is lit when the pin sinks current. This means that driving the pin output low turns the LED on. To control the pin's behavior, we will use a Pin Component, which allows control of the pin using a Component Configuration tool and high-level Component APIs.



Procedure: Initial Kit Setup

The PSoC 4 Pioneer Kit connects to the PC over a USB interface. The kit enumerates as a composite device and three separate devices appear under the device manager window in a Windows operating system.

- 1. Plug in your PSoC 4 Pioneer Kit to you PC using the provided USB cable.
- 2. Assuming you had installed the PSoC 4 Pioneer Kit Software at the beginning of the workshop, you will now see the Windows driver-enumeration process begin.
- 3. Wait for the driver installation to complete as depicted in Figures 2a and 2b.

Figure 2a: PSoC 4 Pioneer Kit Driver Installation In Progress

Driver Software Installation		X
Installing device driver softwar	e	
USB Composite Device KitBridge KitProg Programmer KitProg USBUART Obtaining device driver software from V Skip obtaining driver software from Win	Ready to use Searching Windows Update Searching Windows Update Searching Windows Update Vindows Update might take a while.	
		Close

Figure 2b: PSoC 4 Pioneer Kit Driver Installation Complete

Driver Software Installation		X
Your device is ready to use		
USB Composite Device USB Input Device KitProg (3.4.1.20) KitProg USB-UART (COM16)	 Ready to use Ready to use Ready to use Ready to use 	
		Close

Procedure: Firmware

- 1. Open PSoC Creator. It is located in the Cypress -> PSoC Creator folder in the Windows start menu.
- 2. Create a new project by using the "New->Project" option in the "File" Menu. This step is shown in Figure 3.



Figure 3: PSoC Creator Project Creation

::	PSo	oC Crea	ator 2.2	-	-				
E	ile	<u>E</u> dit	<u>V</u> iew	<u>P</u> roject	<u>B</u> uild	<u>D</u> eb	ug	<u>T</u> ools	<u>W</u> indow
		<u>N</u> ew			•	8	<u>P</u> ro	ject	
	L	<u>O</u> pen			•	<u>•</u>	<u>F</u> ile	5	
	Ľ	<u>A</u> dd			Þ				→ ∓ ×
		<u>C</u> lose		Ctr	l+F4				

- 3. The "New Project" dialog will appear. Click on the "Empty PSoC 4 (CY8C42*) Design" option under "Empty Templates." This step is shown in Figure 4.
- 4. Set the name of the project "Lab 1 Blink" or something similar. Click "OK" to create the project.

Figure 4: PSoC Creator "New Project" Dialog

New Project	
Design Other	4
Empty Templates	
Empty PSoC 3 Design	Creates a PSoC 3, 8 bit, design project.
Empty PSoC 4 Design	Creates a PSoC 4, 32 bit, design project.
Empty PSoC 5LP Design	Creates a PSoC 5LP, 32 bit, design project.
PSoC 3 Starter Designs	
ADC_DMA_VDAC	Shows how to transfer data from an ADC to a DAC using DMA with no CPU intervention.
DelSig_16Channel	Shows a 16-channel, 12-bit Delta Sigma ADC in PSoC 3 sequenced in hardware; samples are transferred from ADC to SRAM using DMA - without processor intervention.
▶ DelSig_I2CM	Shows the 16-bit differential ADC, hardware multiplexed into 8 channels and transported over I2C.
DelSig_I2CS	Shows the 16-bit differential ADC, hardware multiplexed into 8 channels and transported over I2C.
▶ DelSig_SPIM	Shows the 16-bit differential ADC, hardware multiplexed into 8 channels and transported over SPI.
Filter_ADC_VDAC	Shows how to filter an analog input all in hardware, and provides all the DMA setup to transfer the data from the ADC directly to the Digital Filter Block, bypassing the processor.
HW_Fan_Control_with_Alert	Shows how performing fan control in hardware completely frees up the CPU.
PSoC 4 Starter Designs	
ADC_Differential_Preamplifier	Shows how to sample four different channels with Sequencing ADC and send results to PC using UART.
► CapSense_LowPower	For PSoC 4000 devices only, this project demonstrates a low power CapSense system using PSoC 4.
CapSense_Proximity_Design	For PSoC 4000 devices only, this project demonstrates a CapSense based proximity sensing design to control brightness of a LED.
Hibernate_and_Stop_PowerModes	Shows how to enter and wake up from hibernate and stop low power modes.
Multiplexed_Comparator	Shows how to monitor multiple inputs using multiplexed comparator without CPU intervention.
Opamp_Dynamic_Gain_Switching	Shows how to sample three ADC channels, change the input and gain on the fly, and send the result to PC using UART.
PMBusThermExample	Demonstrates usage of the PMBus Slave component in a simulated Thermal Management application.
PSoC 5LP Starter Designs	
Name: Design03	
Location: C:\Users\pkx\Desktop\P4CU/	4Wkshp
+ Advanced	
	OK Cancel



5. You will then be taken to the PSoC Creator project schematic ("TopDesign.cysch"). The right hand side of the PSoC Creator IDE will show the Component Catalog. Select the "Digital Output Pin" Component from the "Ports and Pins" category, and drag it into your schematic. This step is shown in Figure 5.

Figure 5: Adding a Digital Output Pin





6. Double-click the Pin Component to configure it using its configuration tool. In this window, rename the pin to "Pin_Red", and <u>deselect</u> the "HW Connection" checkbox to allow us to control it in firmware. Click OK to apply changes and close the configuration tool window. This step is shown in Figure 6.

Figure 6: Pin Component Configuration Tool

Configure 'cy_pins'		? ×				
Name: Pin_Red	Name: Pin_Red					
Pins Mapping	Pins Mapping Clocking Built-in 4 b					
Number of Pins: 1	X 🗗 🕈 🗎 🕅					
[All Pins]	Type General Inpu	it Output				
	Analog	Preview:				
	Digital Input					
	HW Connection					
	Digital Output					
	HW Connection					
	Output Enable					
	Bidirectional					
	Show External Termina	I				
<u> </u>						
Datasheet		OK Apply Cancel				



7. On the left-hand side of the PSoC Creator IDE is the "Workspace Explorer", which shows the files contained within the PSoC Creator project. Double click on the "Lab 1 Blink.cydwr" file to open the Cypress Design Wide Resources (.cydwr) window. This configures aspects of the project that affect the entire part, such as pin mapping, clocks and interrupts. This step is shown in Figure 7.



Figure 7. Design Wide Resources – Pins

8. The "Pins" tab of the Design Wide Resources will be opened by default. Click the drop-down list under the "Port" column, and map the "Pin_Red" pin to P1[6]. This step is shown in Figure 8.

Alias	Name 🕖	P	ort)	Pir	n	Lock
	Pin_Red		-			•	
		P1[0]	TCPWM	12 : P	^		
		P1[1]	TCPWM	12 : N			
		P1[2]	TCPWM	(3 : P			
		P1[3]	TCPWM	(3 : N			
		P1[4]					
		P1[5]					
		P1[6]	N				
		P1[7]	5				
		P2[0]					

Figure 8. Pin Assignment Window



9. Now, back in the "Workspace Explorer", double-click the "main.c" file to open it in the code editor. This step is shown in Figure 9.

Figure 9. PSoC Creator Code Editor Showing "main.c" Code



10. Add the code shown in Code 1 inside the "for(;;)" loop to toggle the LED pin once every half second. The below code reads the pin's current value and writes its inverse back to the pin, followed by a 500ms delay.

Code 1. Lab 1 Pin Toggle Code

Pin_Red_Write(~Pin_Red_Read()); //toggle pin state

CyDelay(500); //system delay of 500ms

11. Press the "Program" button on the PSoC Creator toolbar to build the project and program your kit. This step is shown in Figures 10a. The results of the build and program operation will appear in the "Output" area at the bottom of the PSoC Creator IDE. This is shown in Figure 11. At this point, the red LED on the kit should start blinking at a 1 Hz rate, at 50% duty cycle.



Figure 10a. PSoC Creator "Program" Button



- a. Note: the first time you plug in your PSoC 4 Pioneer Kit to your computer, it will enumerate over USB and link with the correct drivers that get installed with the kit installer. This enumeration and driver scanning process may take a few moments, just like with any other USB device.
- b. You may also see a pop-up window inside PSoC Creator, asking you to confirm which device to program ('Select Debug Target': Figure 10b). Simply chose the "KitProg", the PSoC 4 Pioneer kit as shown below, and then press "Connect."

Figure 10b. PSoC Creator "Select Debug Target" Window

Select Debug Target	
□-√ KitProg/0F0E192D00232400 □-√ PSoC 4 CY8C4245AXI-483	PSoC 4 CY8C4245AXI-483 PSoC 4 (ARM CM0) Silicon ID: 0x0BB11477 Cypress ID: 0x04C81193 Revision: PRODUCTION Target unacquired
Show all targets	Connect
	ок



PSoC 4 Lab 1: Blinking LED Lab Manual

Figure 11. PSoC Creator Output Window

Lab 1 Blink - PSoC Creator 2.2 [C:\\Lab Projects\Lab	1 Blink\Lab 1 Blink.cydsn\TopDesign\TopDesign.cysch]	
<u>File Edit View Project Build Debug Tools</u>	Window Help	
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	🔍 🖕 🔜 👻 🖕 52% 🔹 🔍 🤤 🖳 🖕 Debug 🔹 🦆	
🔛 🕶 🚵 📽 👹 🌺 🖕 Microsoft Sans Serif	• 10 • B I U ≡ ≡ ≡ A • 4 • 0 • , 9 13 13 42 54	4.4.11日。
Workspace Explorer 🗸 🗸 🗙	Start Page TopDesign.cysch main.c Lab 1 Blink.cydwr	- 4 Þ × [0
		in the second se
Workspace 'Lab 1 Blink' (1 Projects)		
Project 'Lab 1 Blink' [CY8C4245AXI-483] General Content of the second		Catalog
Header Files	0	(1.35
Line device.h		Compo
		nents
Generated_Source		
PSoC4 Poc4 Poc4	⊡Pin, Red	
Cm0RealView.scat		
Output		+ 4 X
Show output from: All 🗾 🗸 🛒		
Device ID Check Erasing Programming of Flash Starting Protecting Verify Checksum Device 'PSoC 4 CY8C4245AXI-483' was	successfully programmed at 05/01/2013 11:14:11.	
Output Notice List		
Ready	0 Errors	0 Warnings 0 Notes

Procedure: PSoC 4 Pioneer Kit Hardware Setup

No hardware setup is required for this project. The red LED is connected to P1[6] with a copper trace.

Conclusion

The red LED [D9] is blinking on your PSoC 4 Pioneer Kit at a rate of 1 Hz and a duty cycle of 50%.



Stretch Goals

- 1. Change the delay to change the LED blink rate.
 - a. We have defined the delay between writes as 500 milliseconds. This results in blinking at 1 Hz.
 - b. Try increasing or decreasing the delay as a constant or variable to change the blink rate at design time or at run-time.
- 2. Change the LED pin to drive the other colors.
 - a. We are driving the red LED out of the tri-color array using P1[6]. The green LED is attached to P0[2]. The blue LED is attached to P0[3]. Remember that all 3 LEDs are active low, and that driving the pins low will turn them on.
 - b. Try driving these other LEDs instead of, or along with the red LED.
- 3. Use the debugger to step through code execution.
 - a. PSoC Creator has a built-in debugger that works with the PSoC 4 Pioneer Kit as well as the stand-alone MiniProg3.
 - b. Start debugging by pressing the "debug" icon on the PSoC Creator toolbars.
 - c. Place a breakpoint on the "Pin_Red_Write..." line by clicking in the code editor to the left of the line.
 - d. Step through the loop and observe the LED state changing.



Document Revision History

Revision	Ву	Description
**	MAXK	First Release
*A	GUL	Updated formatting. Added Initial Kit Setup section to procedure
*В	РКХ	Updated for Creator 3.0 and CY8CKIT-049-42XX